

ANNA UNIVERSITY, CHENNAI
AFFILIATED INSTITUTIONS
M.E. APPLIED ELECTRONICS
REGULATIONS – 2017
CHOICE BASED CREDIT SYSTEM
I - VI SEMESTERS (PART - TIME) CURRICULA

SEMESTER I

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
THEORY								
1.	MA5152	Applied Mathematics for Electronics Engineers	FC	4	4	0	0	4
2.	AP5151	Advanced Digital System Design	PC	3	3	0	0	3
3.	AP5152	Advanced Digital Signal Processing	PC	5	3	2	0	4
PRACTICALS								
4.	AP5111	Electronic System Design Laboratory I	PC	4	0	0	4	2
TOTAL				16	10	2	4	13

SEMESTER II

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
THEORY								
1.	AP5252	ASIC and FPGA Design	PC	3	3	0	0	3
2.	AP5291	Hardware – Software Co-design	PC	3	3	0	0	3
3.	AP5292	Digital Image Processing	PC	3	3	0	0	3
PRACTICALS								
4.	AP5211	Electronic System Design Laboratory II	PC	4	0	0	4	2
TOTAL				13	9	0	4	11

SEMESTER III

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
THEORY								
1.	AP5191	Embedded System Design	PC	3	3	0	0	3
2.	AP5101	Sensors, Actuators and Interface Electronics	PC	3	3	0	0	3
3.		Professional Elective I	PC	3	3	0	0	3
TOTAL				9	9	0	0	9

SEMESTER IV

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
THEORY								
1.	AP5251	Soft Computing and Optimization Techniques	PC	3	3	0	0	3
2.		Professional Elective II	PE	3	3	0	0	3
3.		Professional Elective III	PE	3	3	0	0	3
PRACTICALS								
4.	CP5281	Term Paper Writing and Seminar	EEC	2	0	0	2	1
TOTAL				11	9	0	2	10

SEMESTER V

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
THEORY								
1.	AP5301	Advanced Microprocessor and Microcontroller Architecture	PC	3	3	0	0	3
2.		Professional Elective IV	PE	3	3	0	0	3
3.		Professional Elective V	PE	3	3	0	0	3
PRACTICALS								
4.	AP5311	Project Work Phase I	EEC	12	0	0	12	6
TOTAL				21	9	0	12	15

SEMESTER VI

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
PRACTICALS								
1.	AP5411	Project Work Phase II	EEC	24	0	0	24	12
TOTAL				0	0	24	12	

TOTAL NO. OF CREDITS: 70

**PROFESSIONAL ELECTIVES (PE)*
SEMESTER III
ELECTIVE I**

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
1.	AP5091	Digital Control Engineering	PE	3	3	0	0	3
2.	AP5001	Computer Architecture and Parallel Processing	PE	3	3	0	0	3
3.	AP5002	CAD for VLSI	PE	3	3	0	0	3
4.	CU5292	Electromagnetic Interference and Compatibility	PE	3	3	0	0	3

**SEMESTER IV
ELECTIVE II**

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
1.	AP5003	VLSI Design Techniques	PE	3	3	0	0	3
2.	AP5071	Nano Electronics	PE	3	3	0	0	3
3.	CU5097	Wireless Adhoc and Sensor Networks	PE	3	3	0	0	3
4.	AP5004	High Performance Networks	PE	3	3	0	0	3

**SEMESTER IV
ELECTIVE III**

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
1.	DS5191	DSP Processor Architecture and Programming	PE	3	3	0	0	3
2.	AP5073	RF System Design	PE	3	3	0	0	3
3.	AP5074	Speech and Audio Signal Processing	PE	3	3	0	0	3
4.	AP5092	Solid State Device Modeling and Simulation	PE	3	3	0	0	3

**SEMESTER V
ELECTIVE IV**

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
1.	CP5292	Internet of Things	PE	3	3	0	0	3
2.	AP5005	System on Chip Design	PE	3	3	0	0	3
3.	AP5093	Robotics	PE	3	3	0	0	3
4.	AP5006	Physical Design of VLSI Circuits	PE	3	3	0	0	3

**SEMESTER V
ELECTIVE V**

SL. NO	COURSE CODE	COURSE TITLE	CATEGORY	CONTACT PERIODS	L	T	P	C
1.	AP5094	Signal Integrity for High Speed Design	PE	3	3	0	0	3
2.	VL5091	MEMS and NEMS	PE	3	3	0	0	3
3.	AP5007	Secure Computing Systems	PE	3	3	0	0	3
4.	AP5008	Pattern Recognition	PE	3	3	0	0	3